ABSTRACT OF THE DISCLOSURE

A three or four pole low-voltage power switch is disclosed wherein the switch is partly provided with a device for detecting ground faults. For this purpose, the current vectorial sum must be produced in the three or four conductors of a monitored network. For the switches of this type, output signals received from Rogowski coils are directed via resistances to an integration capacitor whose voltage forms an input signal of another measuring amplifier representing the current sum of a monitored network. The output signal of the measuring amplifier is, afterwards processed in a known manner in the microprocessor of an excess-current trip.